



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech. in EE/EX**

**SEMESTER-III (2024-2028)**

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEC301	Advanced Programming Concepts	2	0	4	4	60	20	20	30	20	150
2	-	Generic Elective I	4	0	0	4	60	20	20	0	0	100
3	BTEC302	Network Analysis and Synthesis	3	1	2	5	60	20	20	30	20	150
4	BTEE307	Electrical Instrumentation	3	0	2	4	60	20	20	30	20	150
5	BTEC304	Electronic Devices and Circuits	3	0	2	4	60	20	20	30	20	150
6	BTEC308	Design Lab	0	0	4	2	0	0	0	30	20	50
TOTAL			15	1	14	23	300	100	100	150	100	750

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

*(Signature)*

*(Signature)*

Chairperson  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

*(Signature)*

Chairperson  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

*(Signature)*

Controller of Examinations  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

*(Signature)*

Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

*(Signature)*

Vice Chancellor  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore